

ABSTRACT

A circuit card for transmitting signals having one or more long and narrow holes in a ground/power supply plane under a pad, which is connected to a pattern, the hole extending in a direction substantially parallel with a direction of the pattern extending away from the pad. The holes are sized and positioned to substantially minimize a mismatch of impedance between the pad and the pattern so that reflection of transmission signals caused by impedance mismatch can be suppressed.

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